

INFORMATION DISCLOSURE STATEMENT PTO-1449 (PAGE 1 OF 1)				SERIAL NUMBER 10/629,793		DOCKET NO. P56905	
				APPLICANT YOSHITAKA TERA0 et al.			
				FILING DATE 30 July 2003		GROUP 2879	
<div style="border: 1px solid black; border-radius: 50%; width: 100px; height: 100px; display: flex; align-items: center; justify-content: center; margin: 0 auto;"> <div style="text-align: center;"> U.S. PATENT DOCUMENTS <small>U.S. PATENT & TRADEMARK OFFICE</small> <small>JUN 01 2004</small> </div> </div>							
EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
AN	RE37,444	11/01	Kanazawa				
	5,541,618	07/96	Shinoda				
	5,661,500	08/97	Shinoda et al.				
	5,663,741	09/97	Kanazawa				
	5,674,553	10/97	Sinoda et al.				
	5,724,054	03/98	Shinoda				
	5,786,794	07/98	Kishi et al.				
	5,952,782	09/99	Nanto				
	6,630,916	10/03	Shinoda				
	AN	6,707,436	03/04	Setoguchi et al.			
FOREIGN PATENT DOCUMENTS						TRANSLATION	
	DOCUMENT NUMBER	DATE	COUNTRY	CLAS	SUBCLASS	YES	NO
AN	JP 2001-325888	11/01	JAPAN			Abstract	
AN	JP 2001-043804	02/01	JAPAN			Abstract	
AN	JP 2917279	04/99	JAPAN			Abstract	
	JP 02-148645	06/90					
	JP 2845183	10/98					
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
AN	"Final Draft International Standard", Project No. 47C/61988-1/Ed.1; Plasma Display Panels - Part 1: Terminology and letter symbols, published by International Electrotechnical Commission, IEC. in 2003, and Appendix A - Description of Technology, Annex B - Relationship Between Voltage Terms And Discharge Characteristics; Annex C - Gaps and Annex D - Manufacturing <i>no month</i>						
EXAMINER: <i>Joseph Williams</i>				DATE CONSIDERED: <i>8/5/05</i>			
<small>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							